

PHASE CONTROL THYRISTOR T183-2000

<ul style="list-style-type: none"> ◆ $V_{DRM} = \underline{4400 - 5200 V}$ ◆ $V_{RRM} = \underline{4400 - 5200 V}$ ◆ $I_{T(AV)} = \underline{2620 A}$ ($T_C = 70\text{ °C}$) ◆ $I_{T(AV)} = \underline{2140 A}$ ($T_C = 85\text{ °C}$) ◆ $I_{TSM} = \underline{44 kA}$ ($T_j = 125\text{ °C}$) 		
<ul style="list-style-type: none"> ◆ Amplifying gate ◆ Low on-state switching losses ◆ Acceptable for series and parallel connections ◆ Low dispersion Q_{rr}, V_{TM} 		

MAXIMUM RATED VALUES

Parameter and conditions	Symbol	Values	Units
Repetitive peak off-state voltage, $T_j = -60 \dots +125\text{ °C}$	V_{DRM}	4400 - 5200	V
Repetitive peak reverse voltage, $T_j = -60 \dots +125\text{ °C}$	V_{RRM}	4400 - 5200	
Non-repetitive peak off-state voltage, $T_j = -60 \dots +125\text{ °C}$	V_{DSM}	4500 - 5300	
Non-repetitive peak reverse voltage, $T_j = -60 \dots +125\text{ °C}$	V_{RSM}	4500 - 5300	
Repetitive peak off-state current/ Repetitive peak reverse current, $T_j = 125\text{ °C}$, $V_D / V_R = V_{DRM} / V_{RRM}$	I_{DRM} / I_{RRM}	250	mA
Average on-state current, $f = 50\text{ Hz}$, double side cooling, $T_C = 85\text{ °C}$ $T_C = 70\text{ °C}$	$I_{T(AV)}$	2620 2140	A
RMS on-state current, $T_C = 70\text{ °C}$, $f = 50\text{ Hz}$	I_{TRMS}	4116	A
Surge non-repetitive on-state current, $T_j = 125\text{ °C}$, $V_R = 0$, $t_p = 10\text{ ms}$	I_{TSM}	44	kA
Safety factor	I^2t	$9.7 \cdot 10^6$	A^2s
Critical rate of rise of on-state current, $T_j = 125\text{ °C}$, $V_D = 0.67V_{DRM}$, $I_T = 4000\text{ A}$, $I_{FG} = 2\text{ A}$, $t_r = 1\text{ }\mu s$, $f = 50\text{ Hz}$	$(di_T/dt)_{crit}$	200	$A/\mu s$
Critical rate of rise of off-state voltage, $T_j = 125\text{ °C}$, $V_D = 0.67V_{DRM}$	$(dv_D/dt)_{crit}$	1600 - 2000	$V/\mu s$
Gate power loss, DC	P_{GM}	4	W
Operation junction temperature range	T_j	-60... +125	°C
Storage temperature range	T_{stg}	-60... +50	

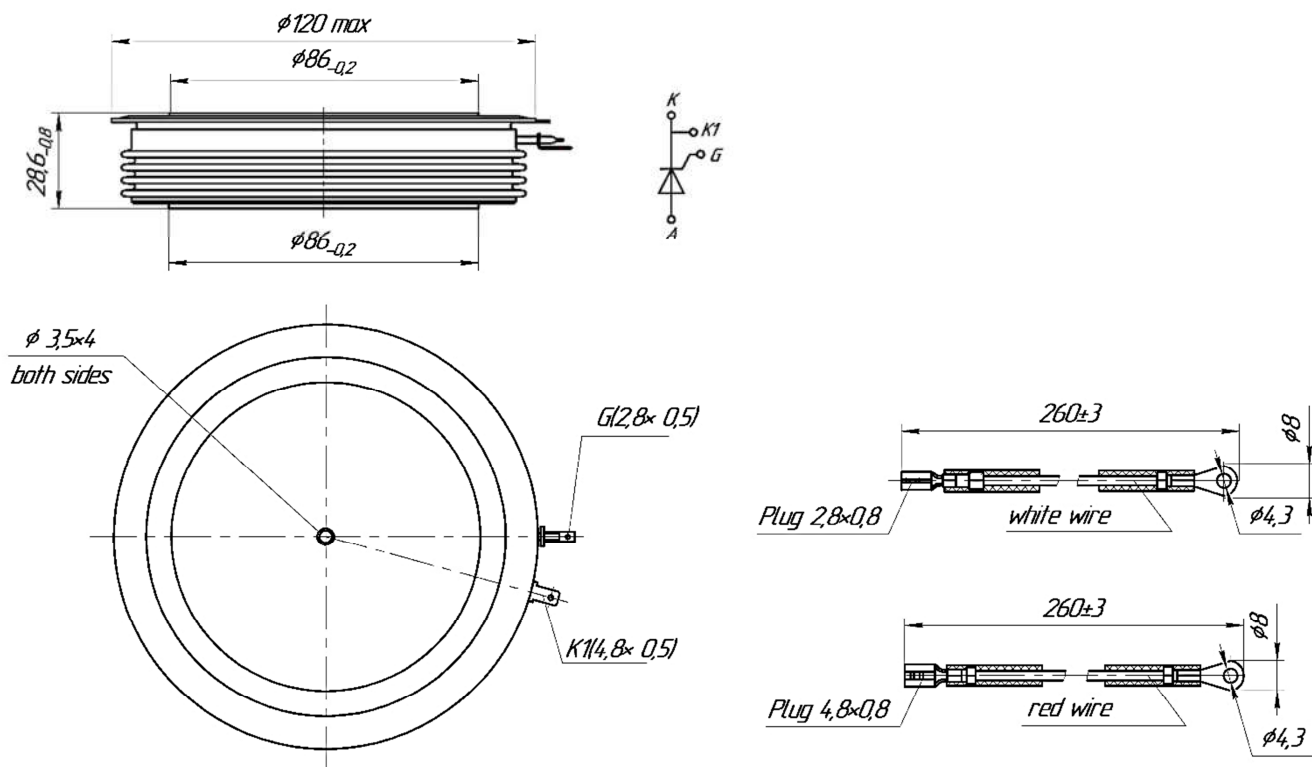


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ELECTRICAL CHARACTERISTICS					
Parameter and conditions	Symbol	Values			Units
		min	typ.	max	
Peak on-state voltage, $T_j = 25\text{ °C}$, $I_T = 6280\text{ A}$	V_{TM}	-	-	2.40	V
On-state threshold voltage, $T_j = 125\text{ °C}$, $I_T = 3140 - 9425\text{ A}$	$V_{T(TO)}$	-	-	1.07	
On-state slope resistance, $T_j = 125\text{ °C}$, $I_T = 3140 - 9425\text{ A}$	r_T	-	-	0.24	mΩ
Delay time, $T_j = 25\text{ °C}$, $V_D = 0.67V_{DRM}$, $I_T = 2000\text{ A}$, $I_{FG} = 2\text{ A}$, $t_r = 0.5\text{ μs}$	t_d	-	-	4.0	μs
Turn off-time, $T_j = 125\text{ °C}$, $I_T = 2000\text{ A}$, $di_T/dt = -5\text{ A/μs}$, $V_R \geq 100\text{ V}$, $V_D = 0.67V_{DRM}$, $dv_D/dt = 50\text{ V/μs}$	t_q	-	630	-	
Reverse recovery charge, $T_j = 125\text{ °C}$, $I_T = 2000\text{ A}$, $di_T/dt = -5\text{ A/μs}$, $V_R \geq 100\text{ V}$	Q_{RR}	-	-	5000	μAs
Holding current, $T_j = 25\text{ °C}$, $V_D = 12\text{ V}$	I_H	-	-	300	mA
Latching current, $T_j = 25\text{ °C}$, $V_D = 12\text{ V}$, $I_{FG} = 2\text{ A}$, $t_r = 0.5\text{ μs}$	I_L	-	-	1500	
Gate trigger voltage, $V_D = 12\text{ V}$, $T_j = -60\text{ °C}$ $T_j = 25\text{ °C}$ $T_j = 125\text{ °C}$	V_{GT}	-	-	3.5 2.5 2.0	V
Gate trigger current, $V_D = 12\text{ V}$, $T_j = -60\text{ °C}$ $T_j = 25\text{ °C}$ $T_j = 125\text{ °C}$	I_{GT}	-	-	450 250 200	mA
Gate non-trigger direct voltage, $T_j = 125\text{ °C}$, $V_D = 0.67V_{DRM}$	V_{GD}	0.35	-	-	V
Gate non-trigger direct current, $T_j = 125\text{ °C}$, $V_D = 0.67V_{DRM}$	I_{GD}	15	-	-	mA
THERMAL PARAMETERS					
Thermal junction to case resistance, DC: double side cooled DC: anode side cooled DC: cathode side cooled	$R_{th(j-c)}$ $R_{th(j-cA)}$ $R_{th(j-cK)}$	-	-	0.0080 0.0160 0.0160	°C/W
Thermal case to heatsink resistance, double side cooled single side cooled	$R_{th(c-h)}$	-	-	0.0025 0.0050	
MECHANICAL PARAMETERS					
Weight	w	-	1.8	-	kg
Clamping force	F	60	-	80	kN
Maximum acceleration (at nominal mounting force)	a	-	-	100	m/s ²
Minimal gate-anode distance on insulator surface	D_s	-	33.7	-	mm



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Device Outline Drawing
(dimensions in mm)



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