



AVALANCHE RECTIFIER DIODE DA153-1250

<ul style="list-style-type: none"> ◆ $V_{RRM} = \underline{2200 - 3200\text{ V}}$ ◆ $I_{F(AV)} = \underline{1250\text{ A}}$ ($T_C = 115\text{ °C}$) ◆ $I_{FSM} = \underline{26\text{ kA}}$ ($t_p = 10\text{ms}$) 		
<ul style="list-style-type: none"> ◆ High reability ◆ Guaranteed maximum avalanche power dissipation ◆ Press-pack design 		

MAXIMUM RATED VALUES

Parameter and conditions	Symbol	Values	Units
Repetitive peak reverse voltage, $T_j = -60 \dots +175\text{ °C}$	V_{RRM}	2200-3200	V
Avalanche breakdown voltage, $T_j = -60 \dots +175\text{ °C}$	V_{BR}	2500-3600	
Repetitive peak reverse current, $T_j = 175\text{ °C}$, $V_R = V_{RRM}$	I_{RRM}	50	mA
Maximum average forward current, $T_C = 115\text{ °C}$, $f = 50\text{ Hz}$	$I_{F(AV)}$	1250	A
RMS forward current, $T_C = 115\text{ °C}$, $f = 50\text{ Hz}$	I_{FRMS}	1962	
Surge non-repetitive current, $T_j = 175\text{ °C}$, $V_R = 0$, $t_p = 10\text{ ms}$	I_{FSM}	26	kA
Safety factor	I^2t	$3380 \cdot 10^3$	A^2s
Operation junction temperature range	T_j	-60 ... +175	°C
Storage temperature range	T_{stg}	-60 ... +50	

**DA153-1250**

ELECTRICAL CHARACTERISTICS					
Parameter and conditions	Symbol	Values			Units
		min	typ.	max	
Maximum peak forward voltage, $T_j = 25\text{ °C}$, $I_F = 3900\text{ A}$	V_{FM}	-	-	2,40	V
On-state threshold voltage, $T_j = 175\text{ °C}$, $I_F = 1960 - 5900\text{ A}$	V_{TO}	-	-	1,10	
On-state slope resistance, $T_j = 175\text{ °C}$, $I_F = 1960 - 5900\text{ A}$	r_T	-	-	0,350	mΩ
Rated reverse power dissipation, $T_j = 175\text{ °C}$, $t_p = 100\text{ мкс}$	P_{RSM}	-	-	16,0	kW
THERMAL PARAMETERS					
Thermal resistance junction to case, DC per diode double side cooled anode side cooled cathode side cooled	$R_{th(j-c)}$	-	-	0,020 0,040 0,040	°C/W
Thermal resistance case to heatsink, double side cooled single side cooled	$R_{th(c-h)}$	-	-	0,005 0,010	
MECHANICAL PARAMETERS					
Weight	w	-	0,55	-	kg
Clamping force	F	22	-	26	kN
Maximum acceleration (at nominal mounting torque)	a	-	-	100	m/s ²



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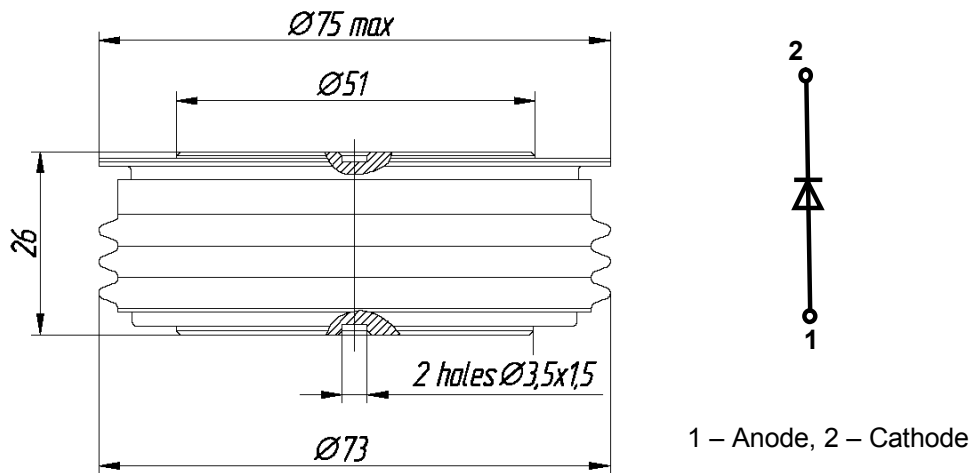


Fig. 1. Device Outline Drawing
(dimensions in mm)



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